

FIG. 1

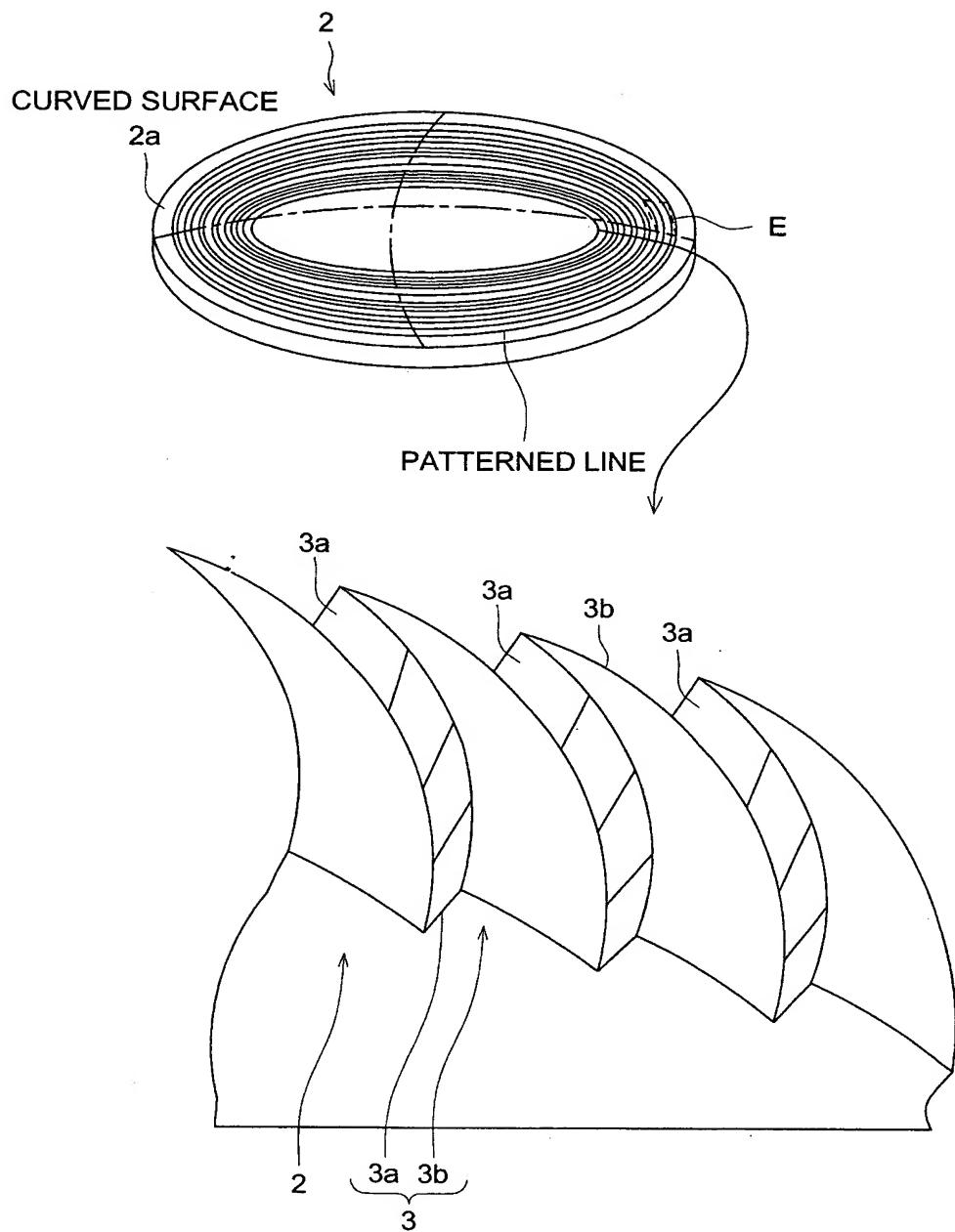


FIG. 2

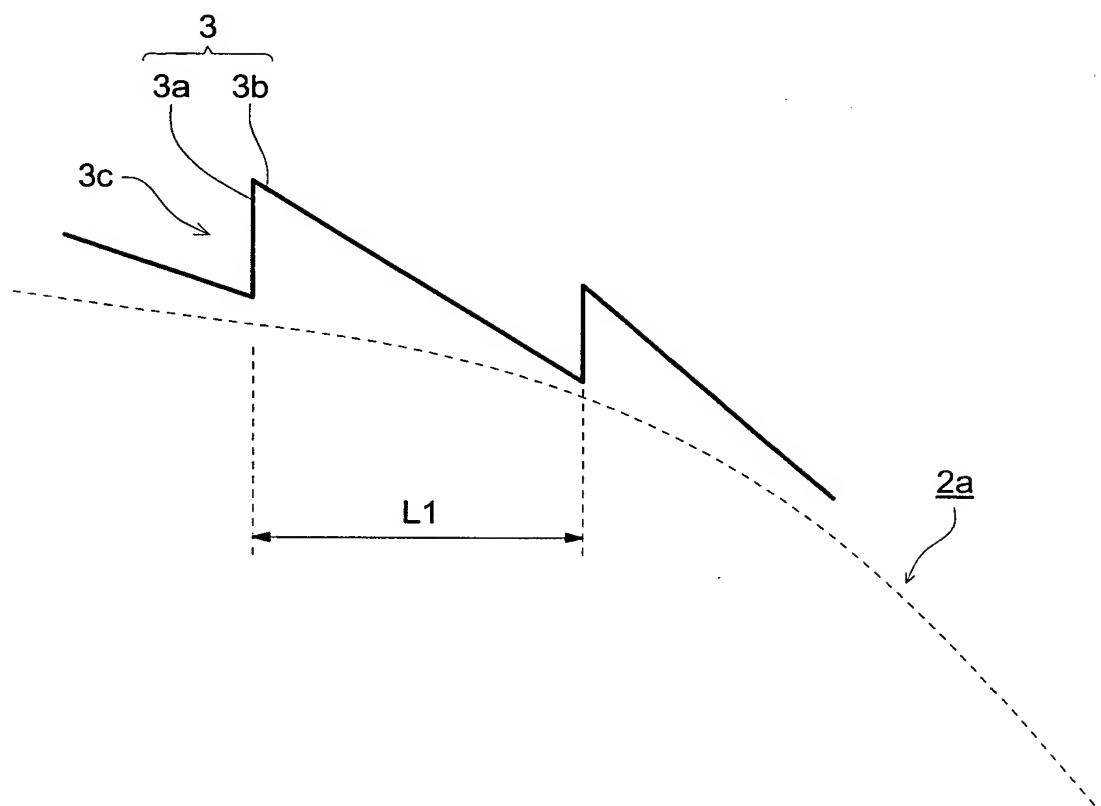


FIG. 3 100 CONTROL CIRCUIT

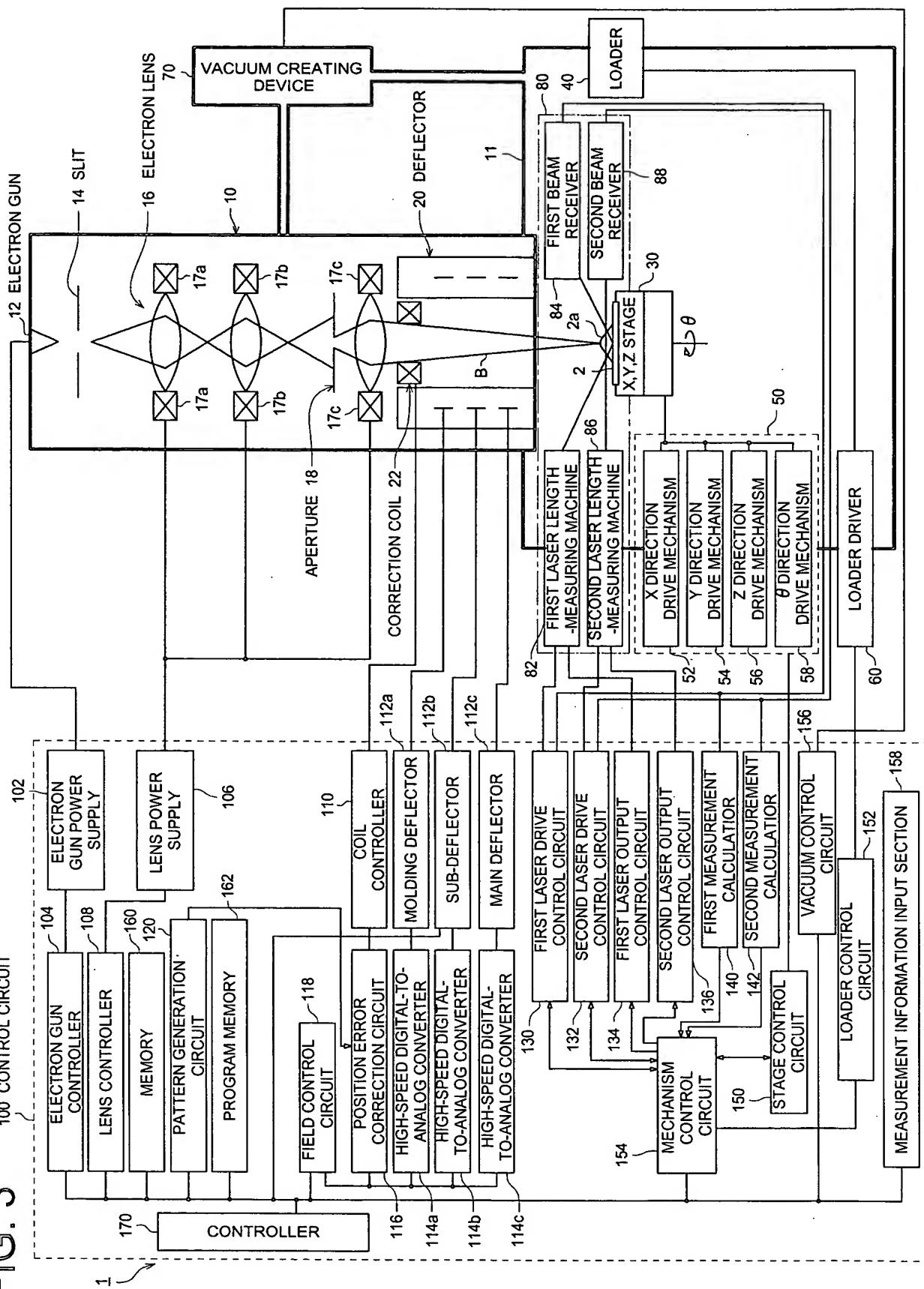


FIG. 4

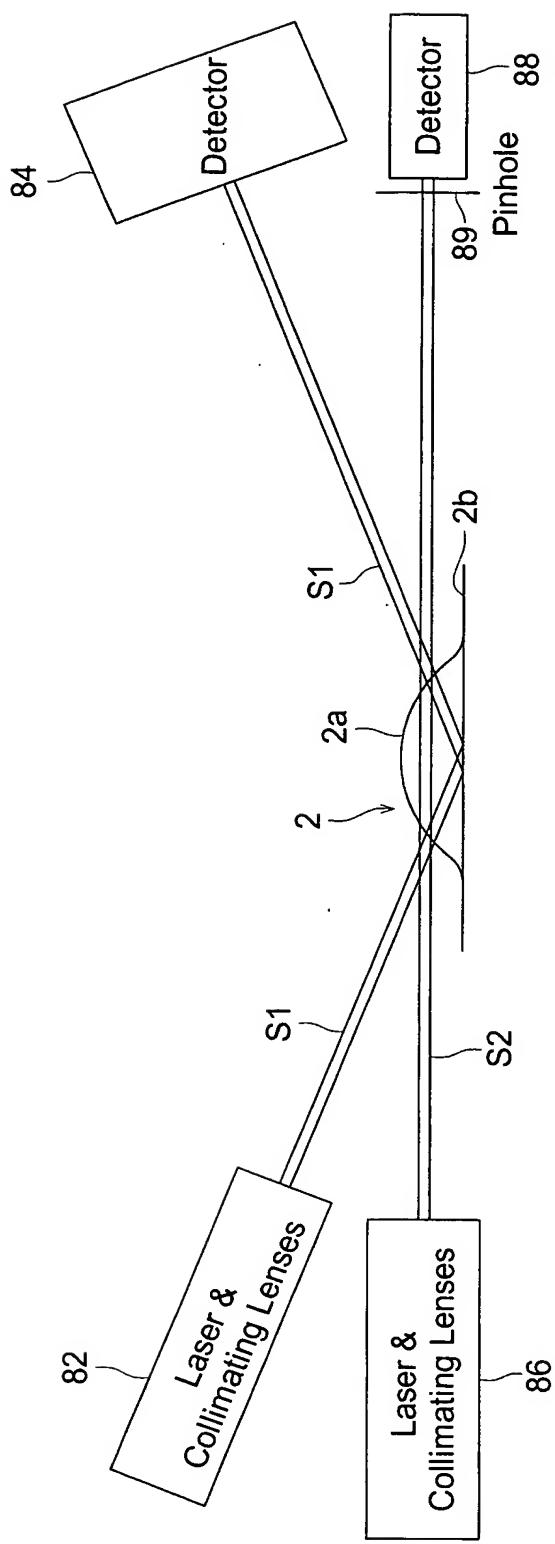


FIG. 5 (A)

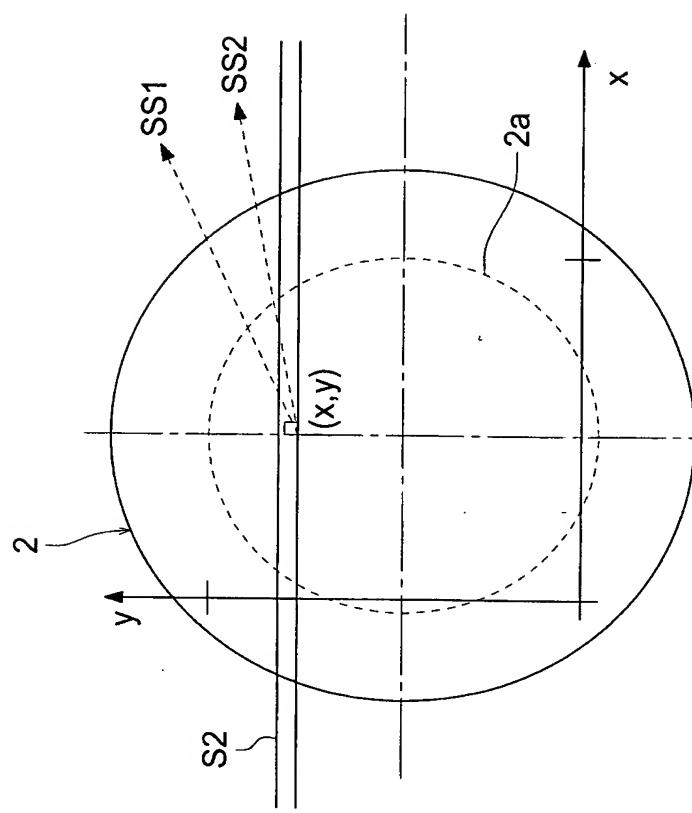


FIG. 5 (B)

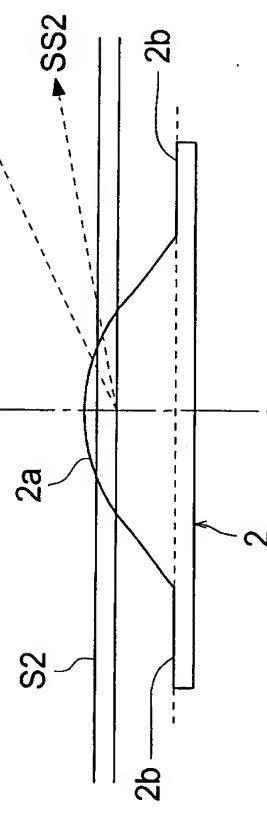


FIG. 5 (C)

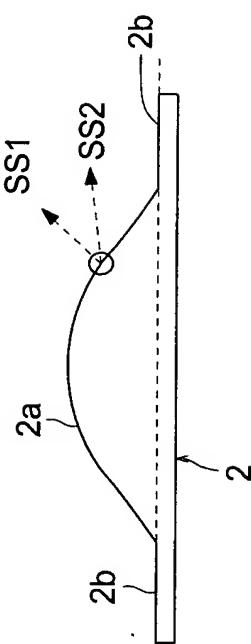


FIG. 6

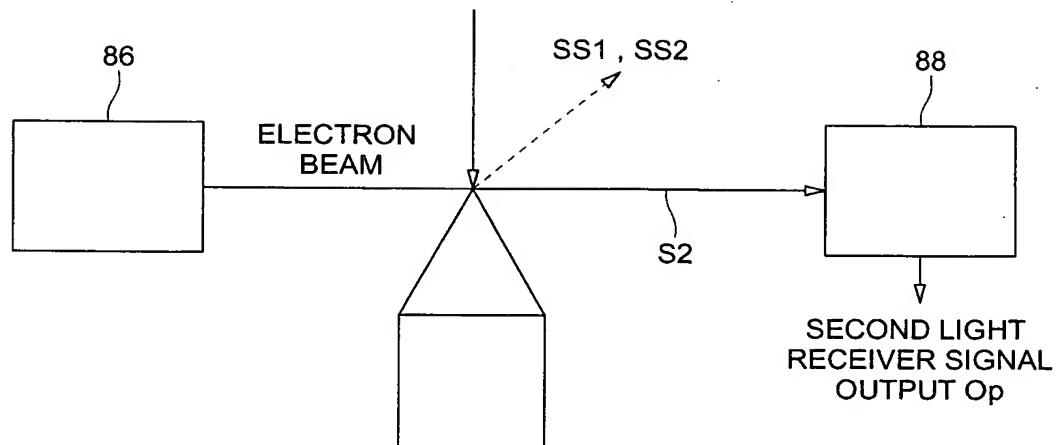


FIG. 7

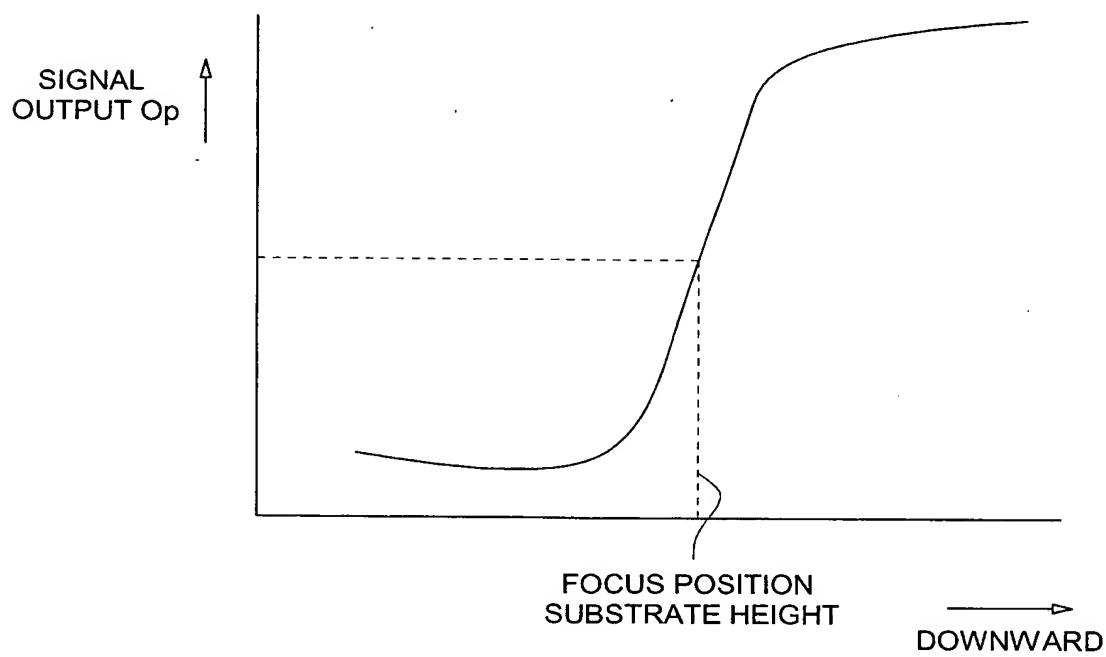


FIG. 8 (A)

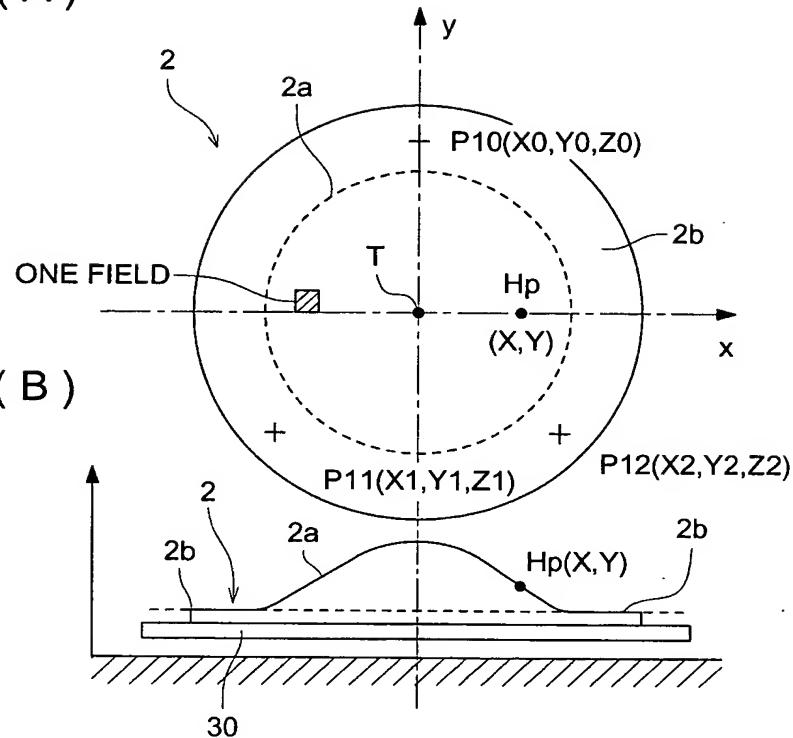


FIG. 8 (B)

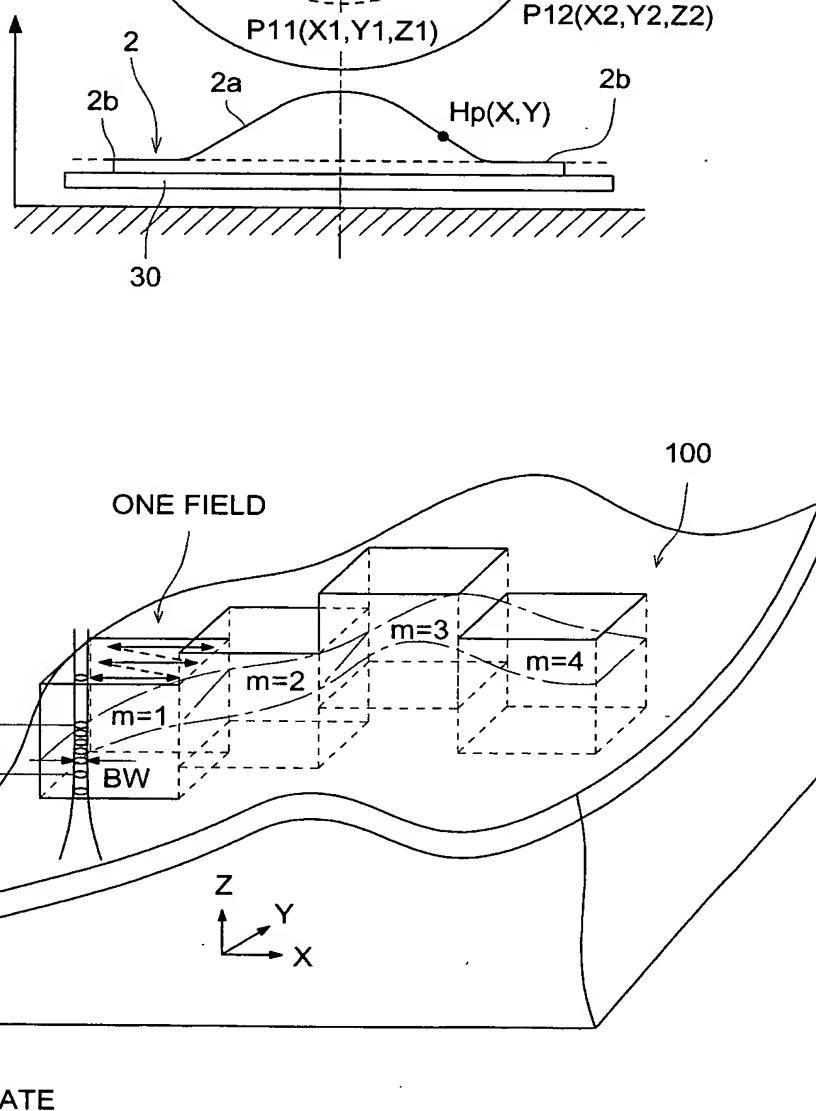


FIG. 9

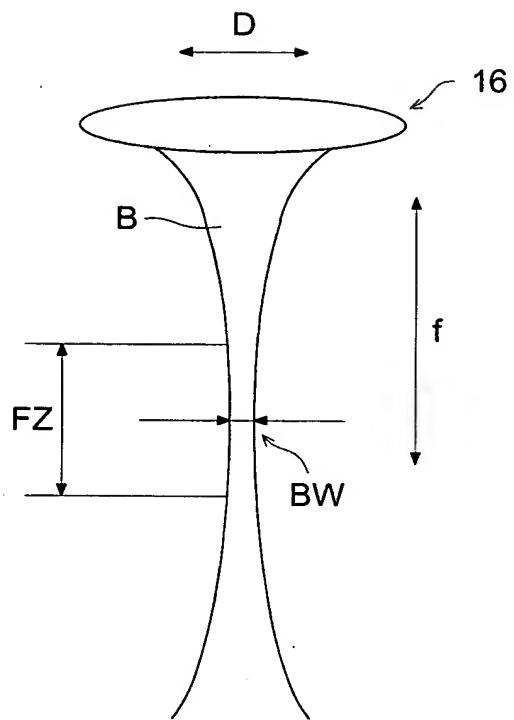


FIG. 10

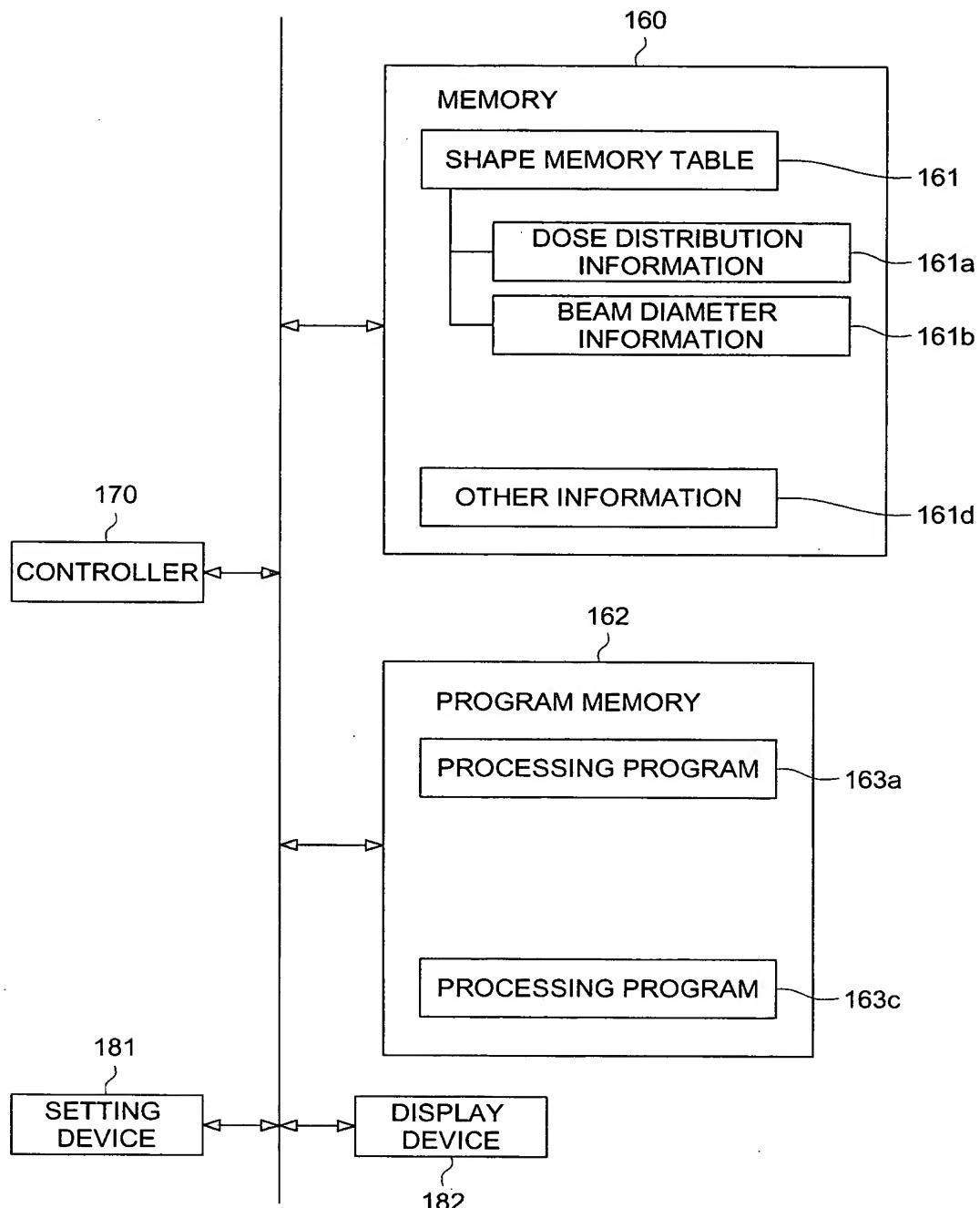


FIG. 11

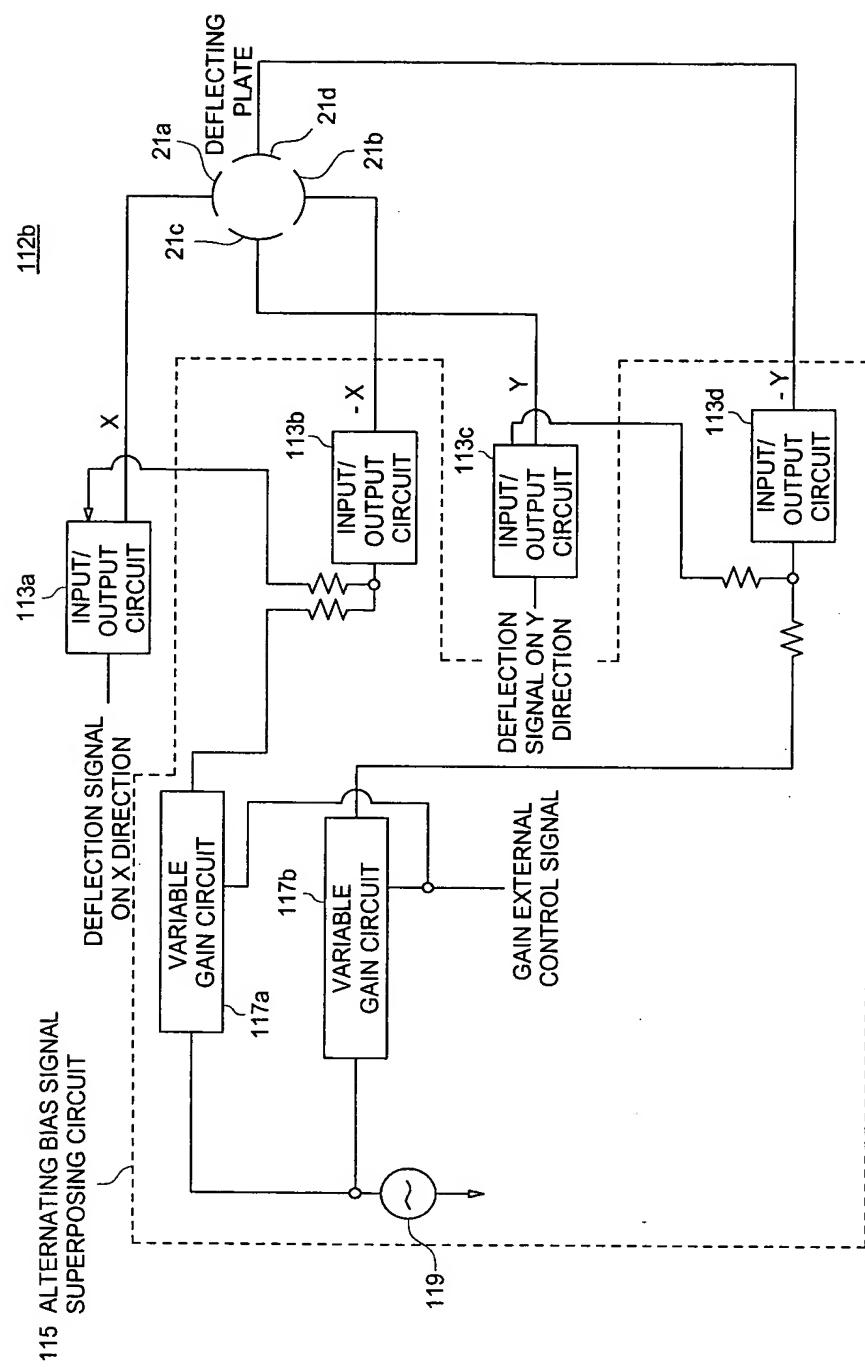


FIG. 12 (A)

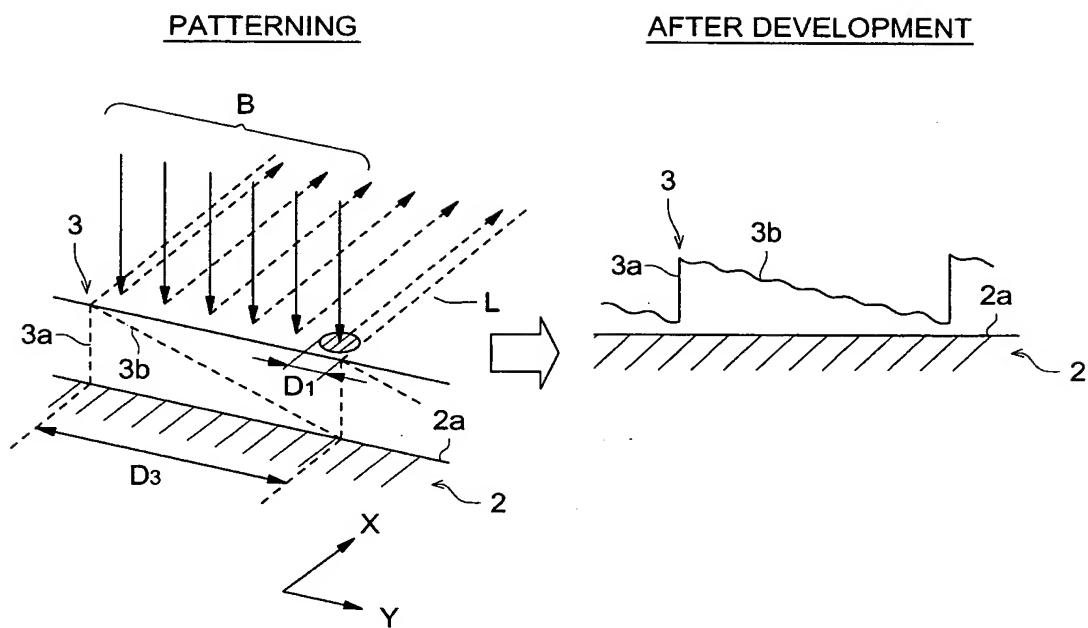


FIG. 12 (B)

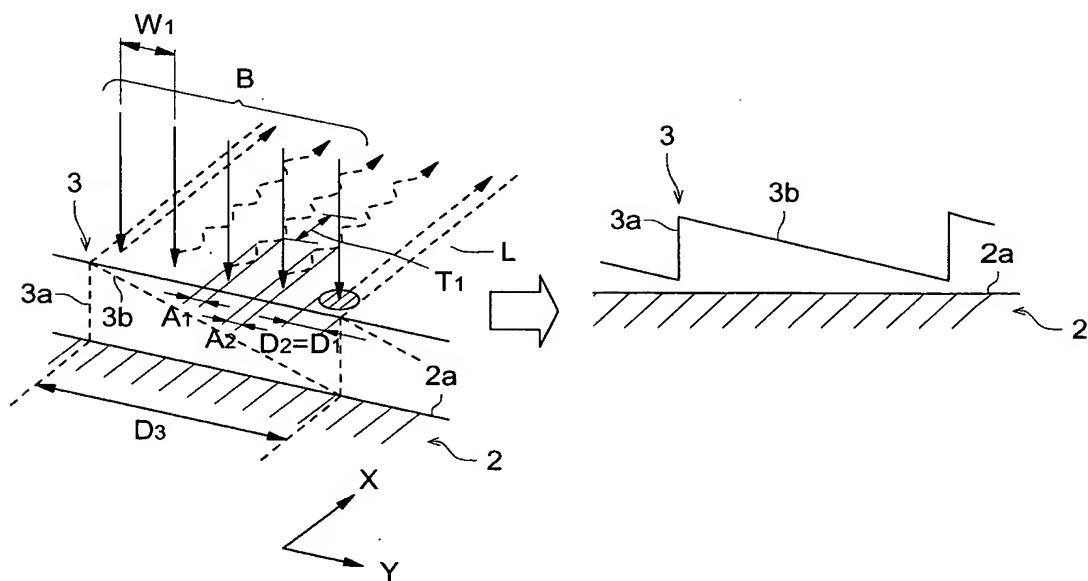


FIG. 13 (A)

(DEFLECTION SIGNAL ACCORDING TO PRIOR ART)

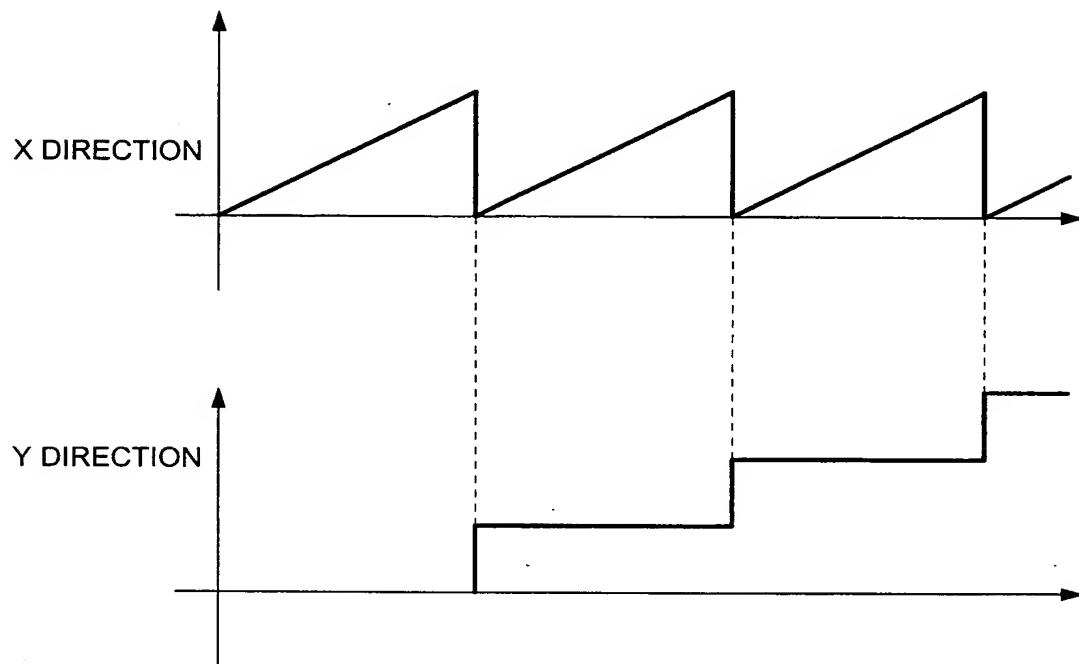


FIG. 13 (B)

(DEFLECTION SIGNAL WITH ALTERNATING BIAS SIGNAL SUPERPOSED THEREON)

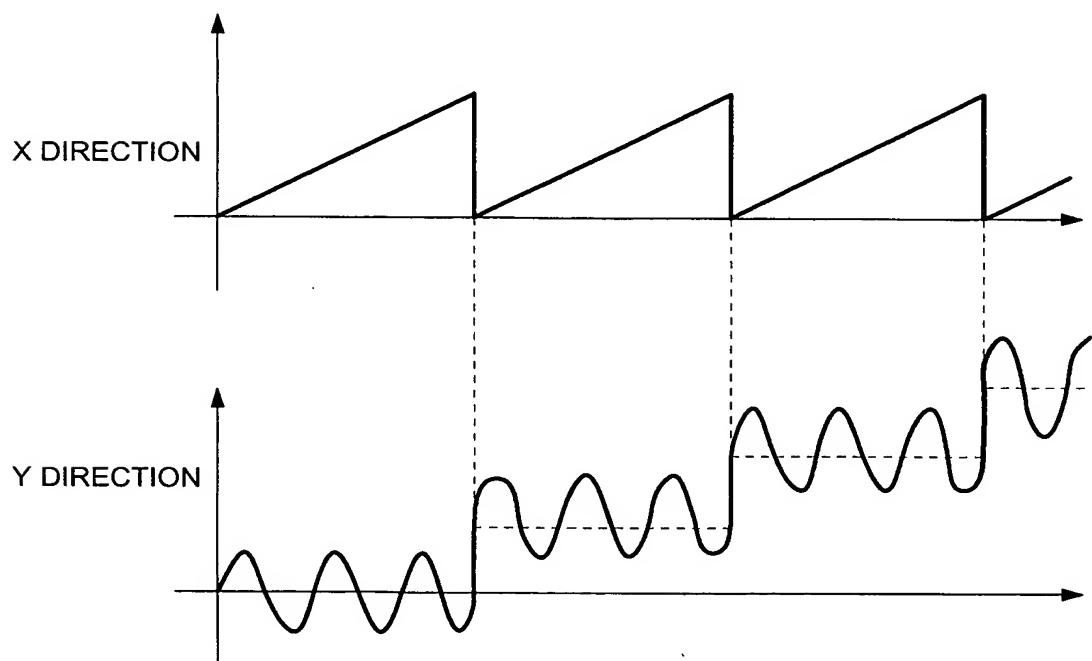


FIG. 14 (A)

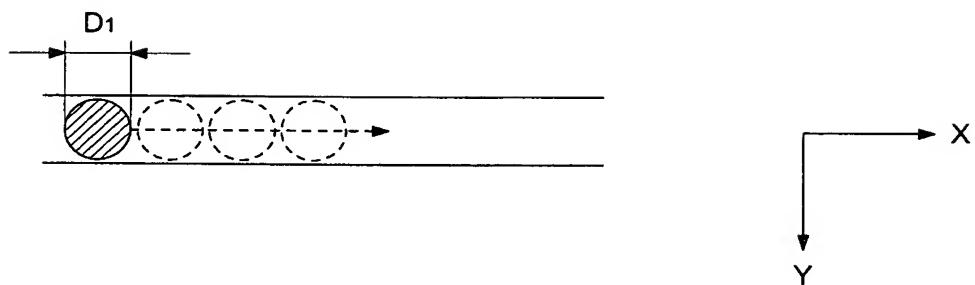


FIG. 14 (B)

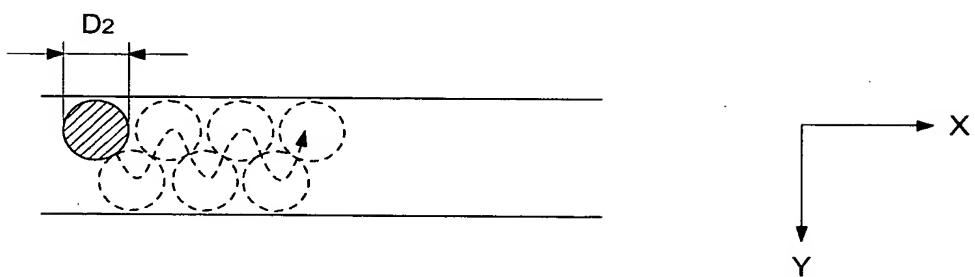


FIG. 15

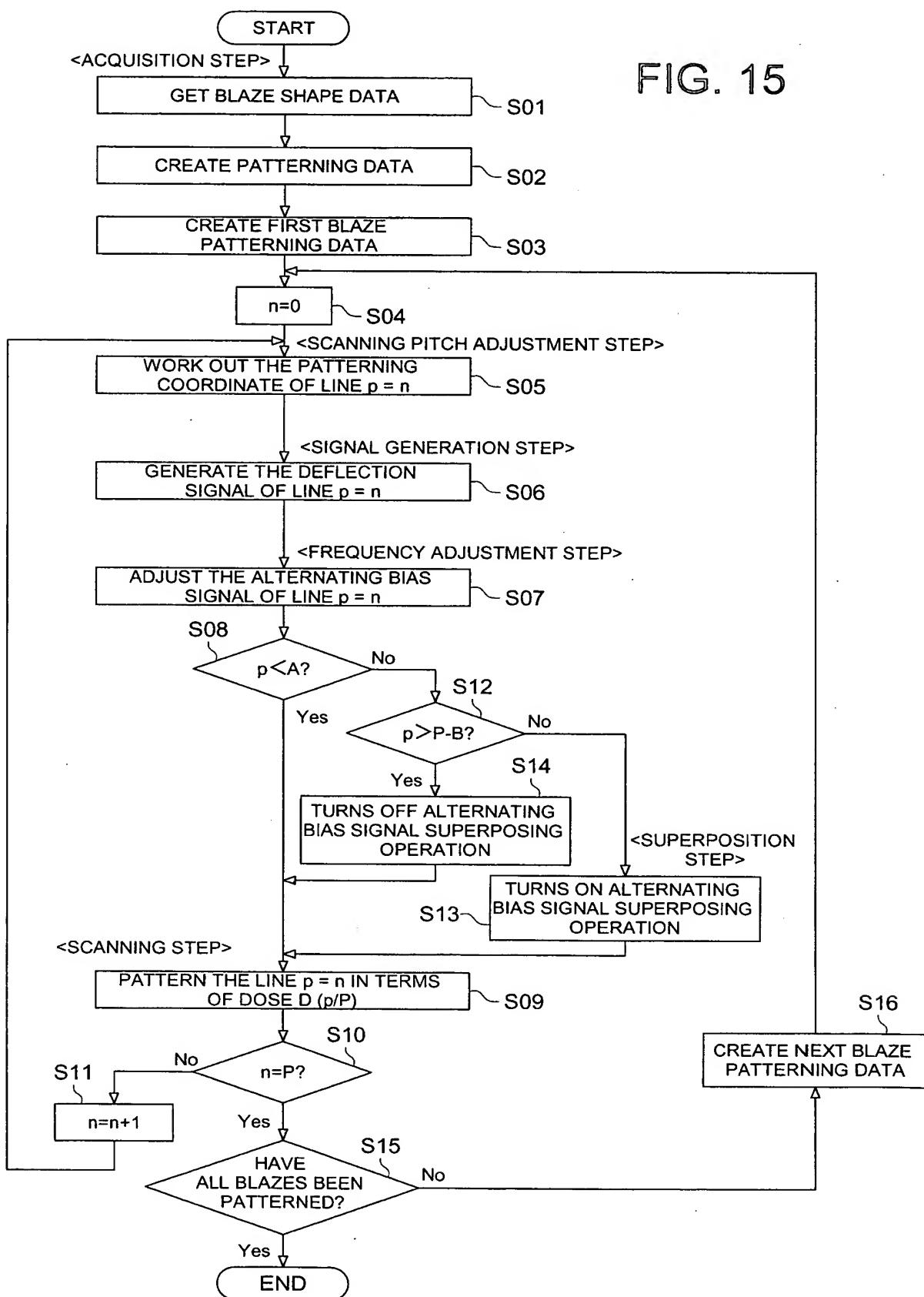


FIG. 16

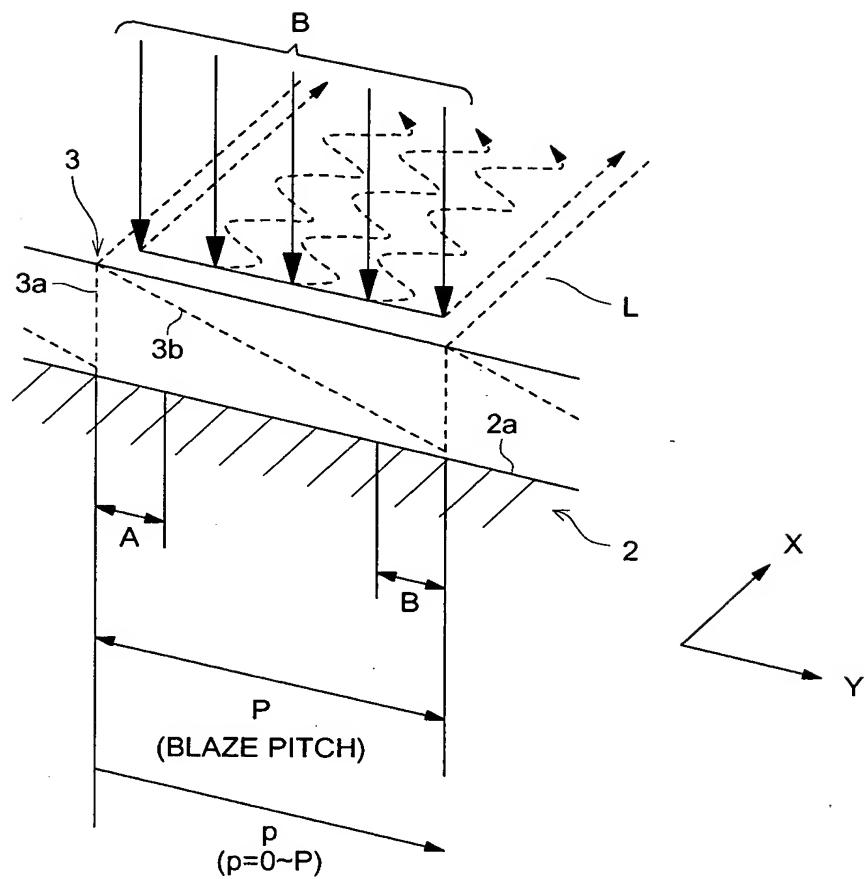


FIG. 17 (A)

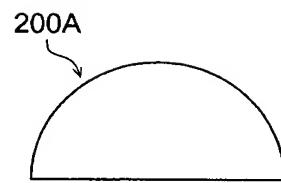


FIG. 17 (B)

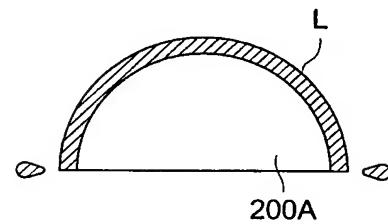


FIG. 17 (C)

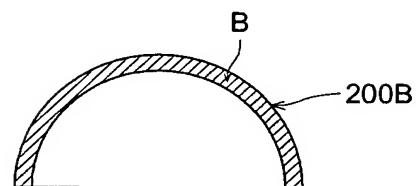


FIG. 17 (D)

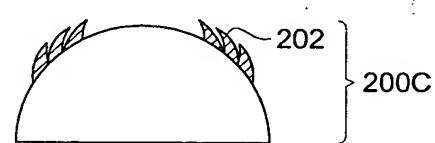


FIG. 17 (E)

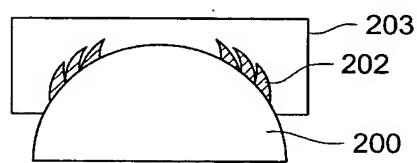


FIG. 17 (F)



FIG. 18 (A)

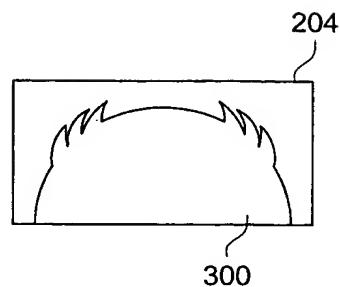


FIG. 18 (B)

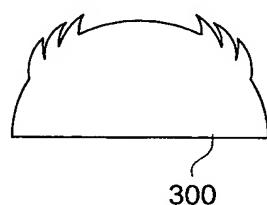
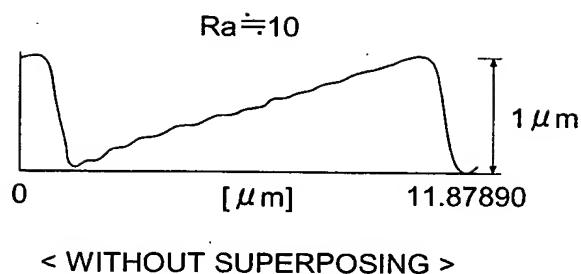
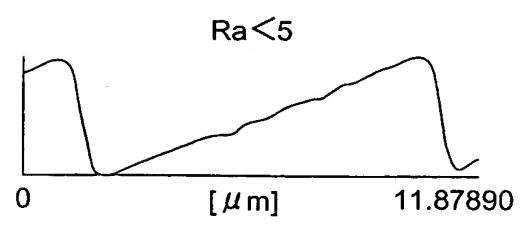


FIG. 19 (A)



< WITHOUT SUPERPOSING >

FIG. 19 (B)



< WITH SUPERPOSING >